

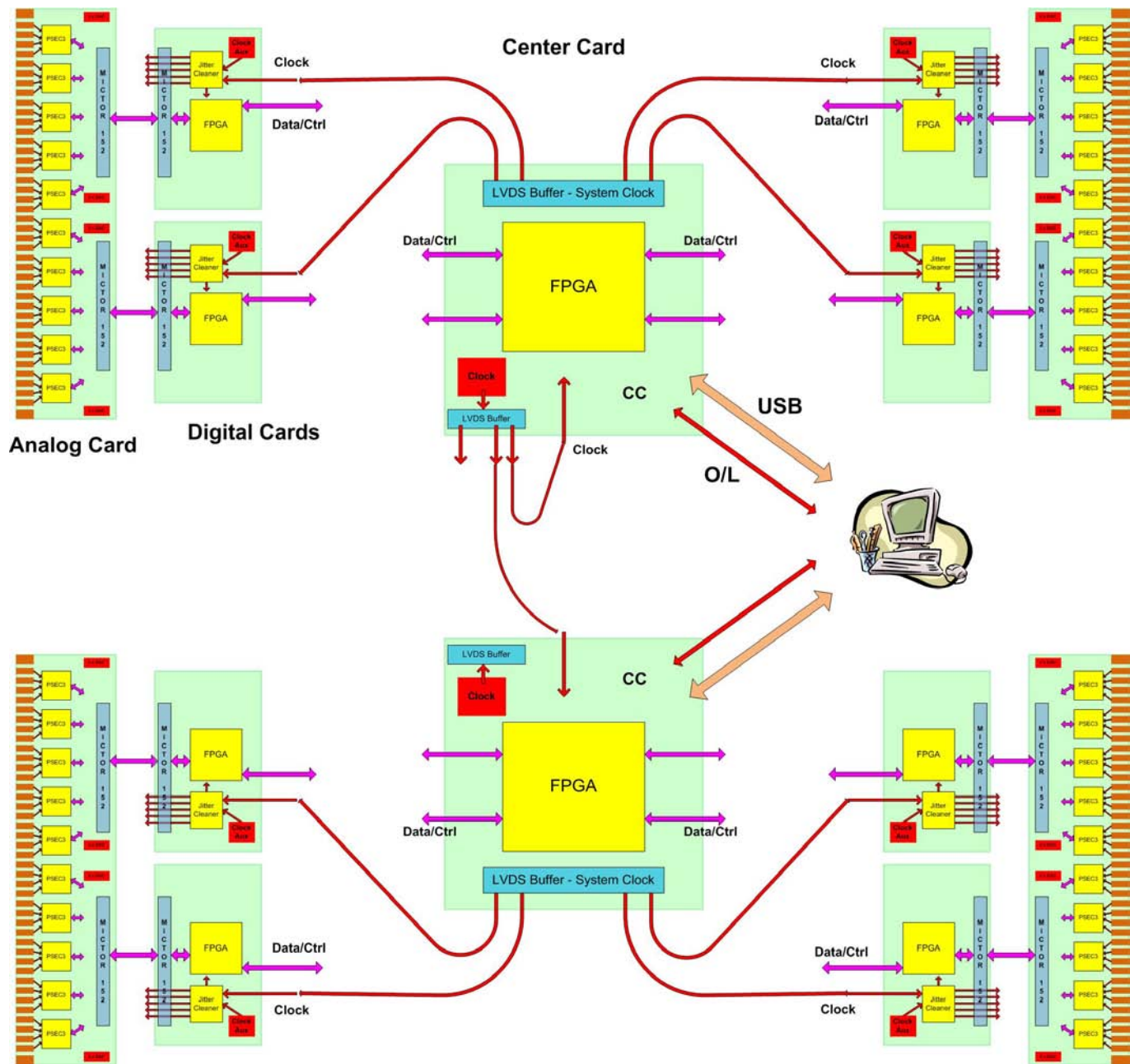
*LAPPD:
Collaboration meeting III
9-Dec-2011*

THE SUPERMODULE READOUT CHAIN: DIGITAL & CENTRAL CARDS

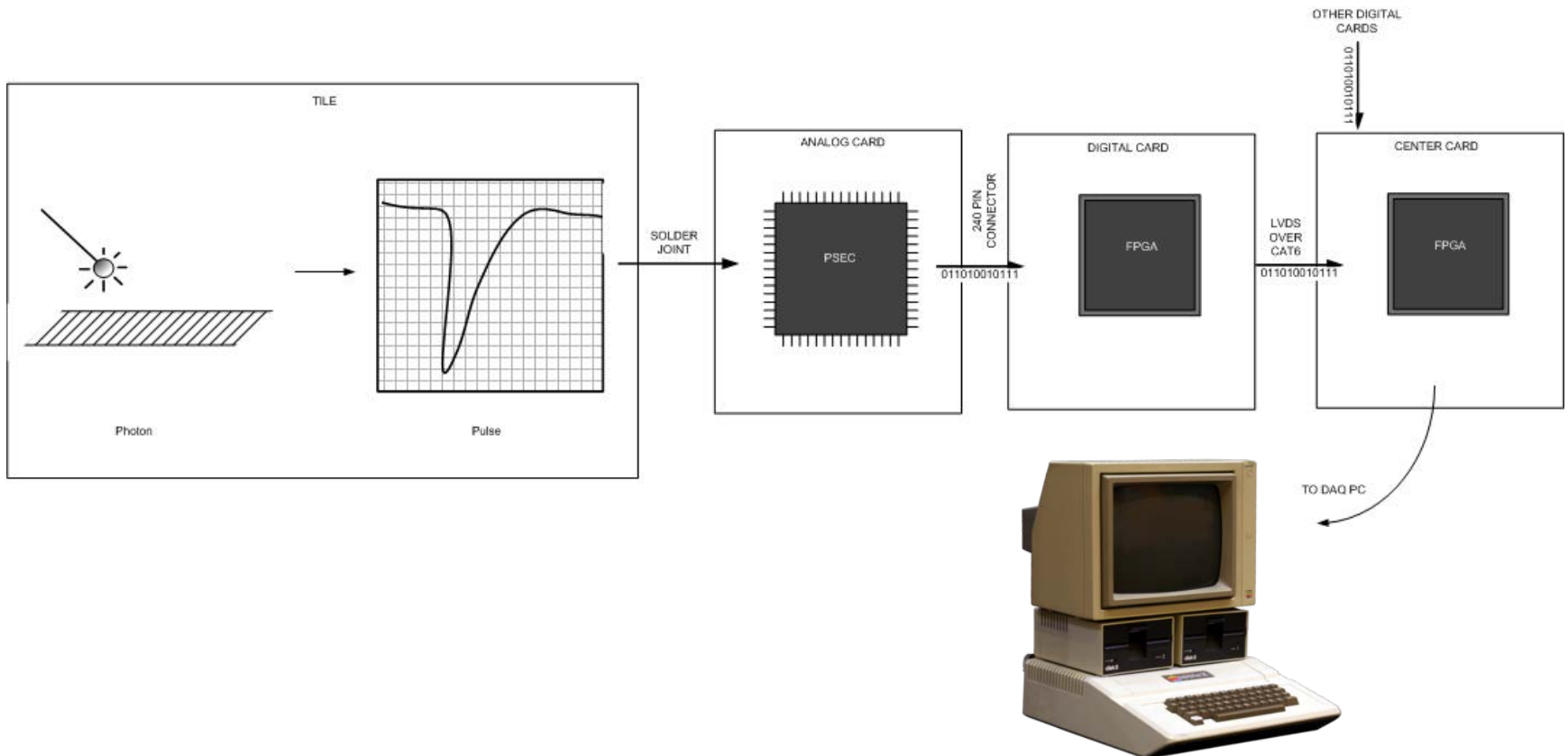
Craig Harabedian
University of Chicago



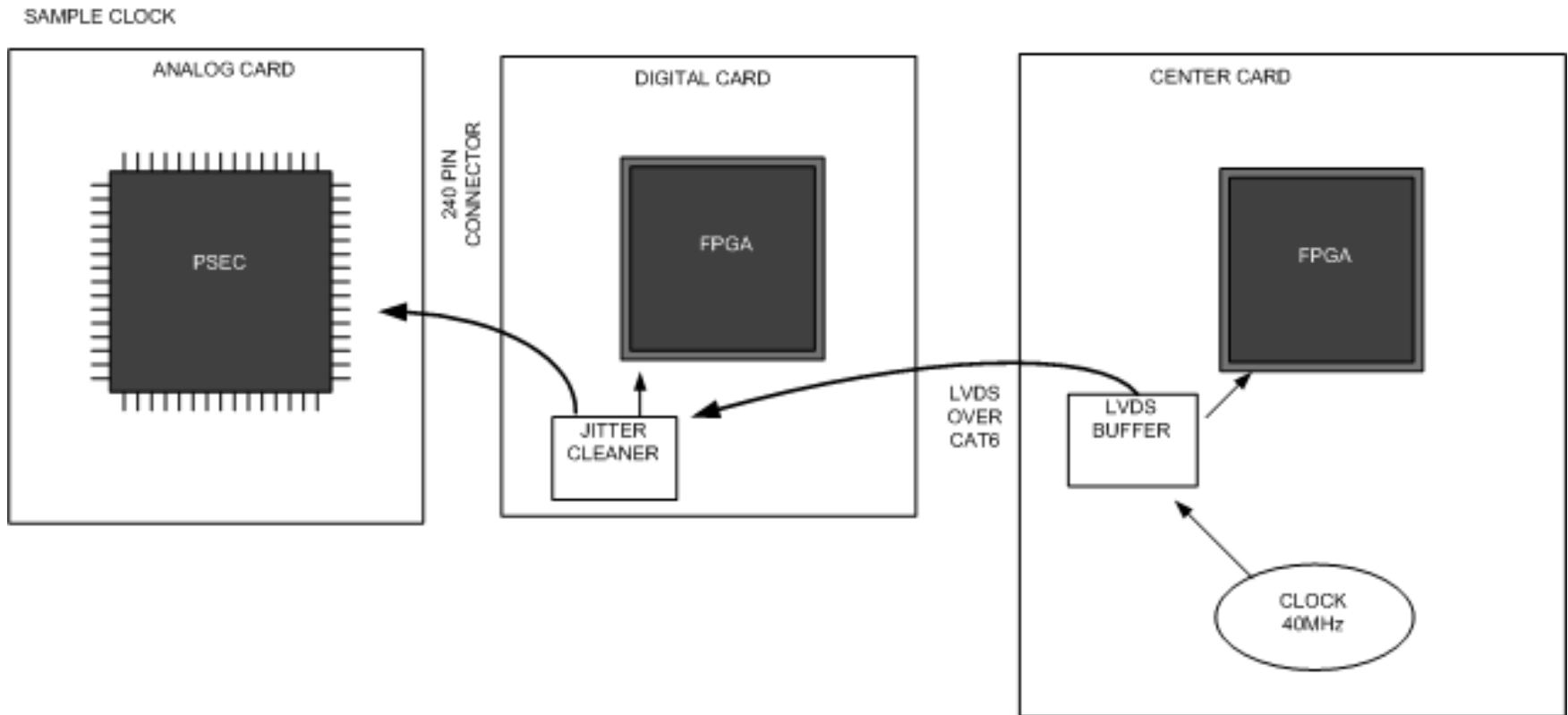
Overview



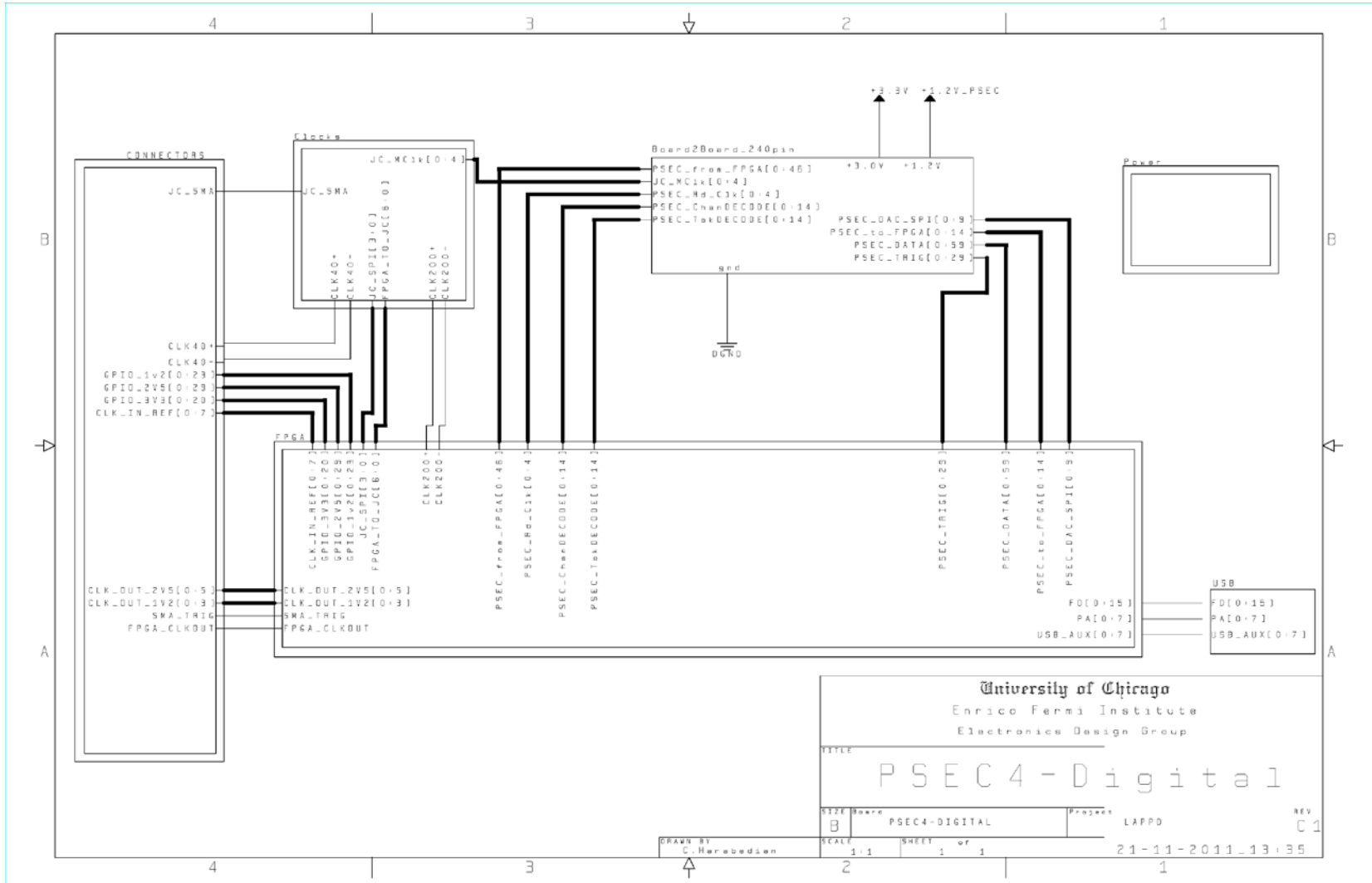
Data Flow



Clock Distribution



Digital Card



Digital Card

